

FIG. 1 (Prior Art)

FIG. 2 (Prior Art)

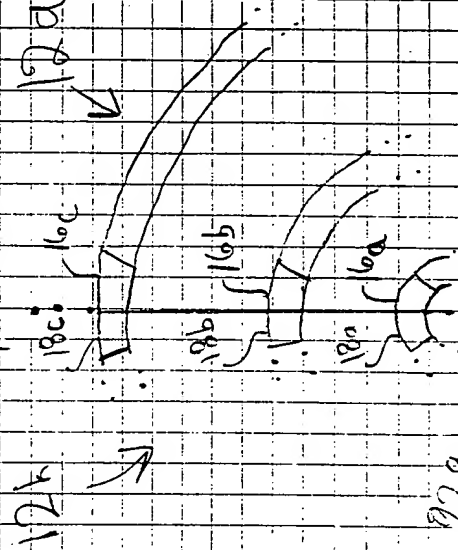


FIG. 3 (Prior Art)

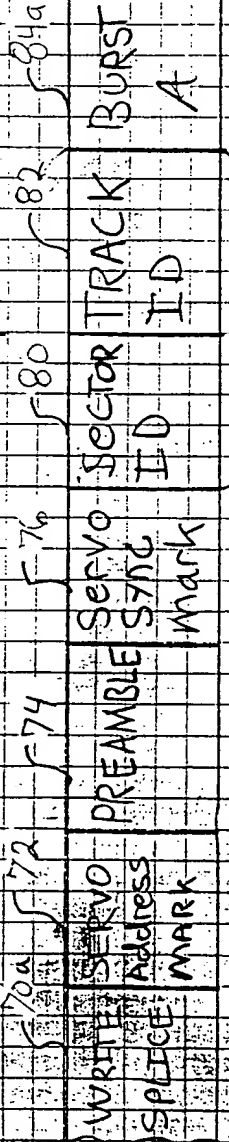
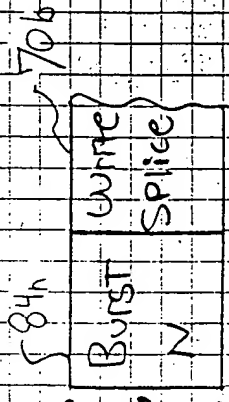
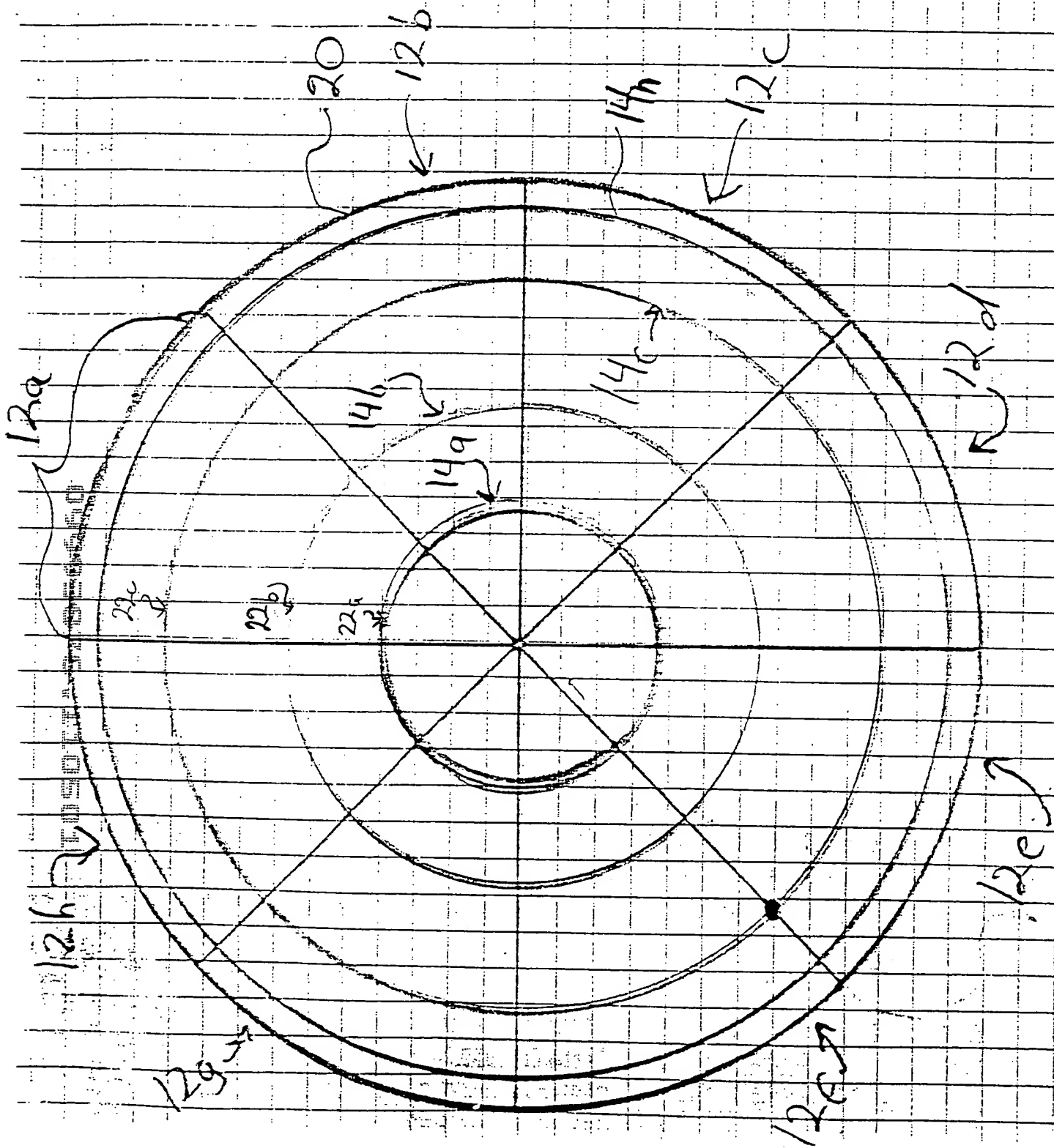


FIG. 6



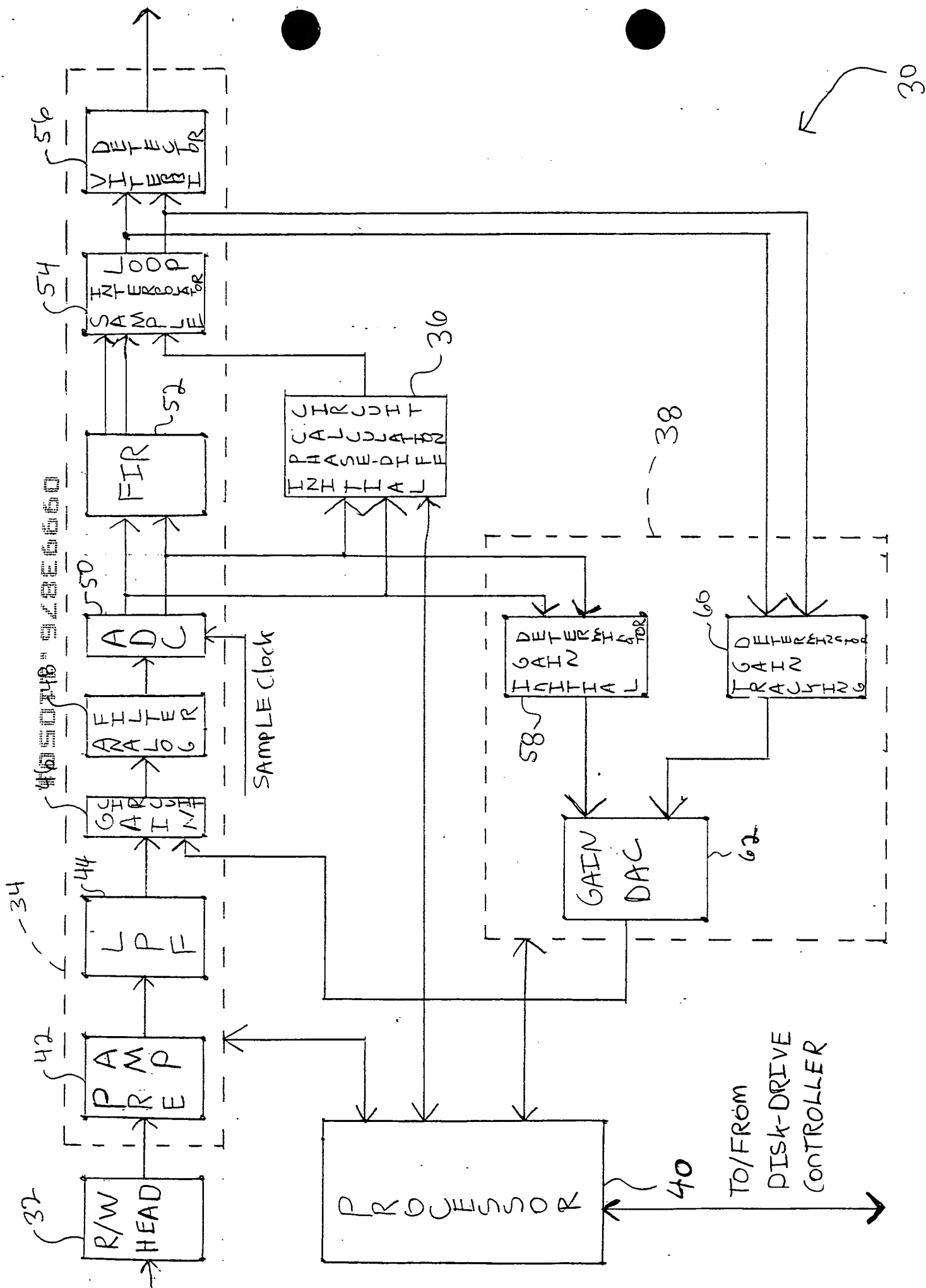


FIG. 5

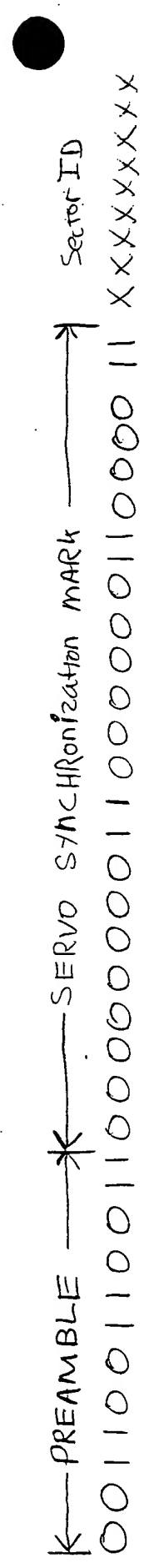
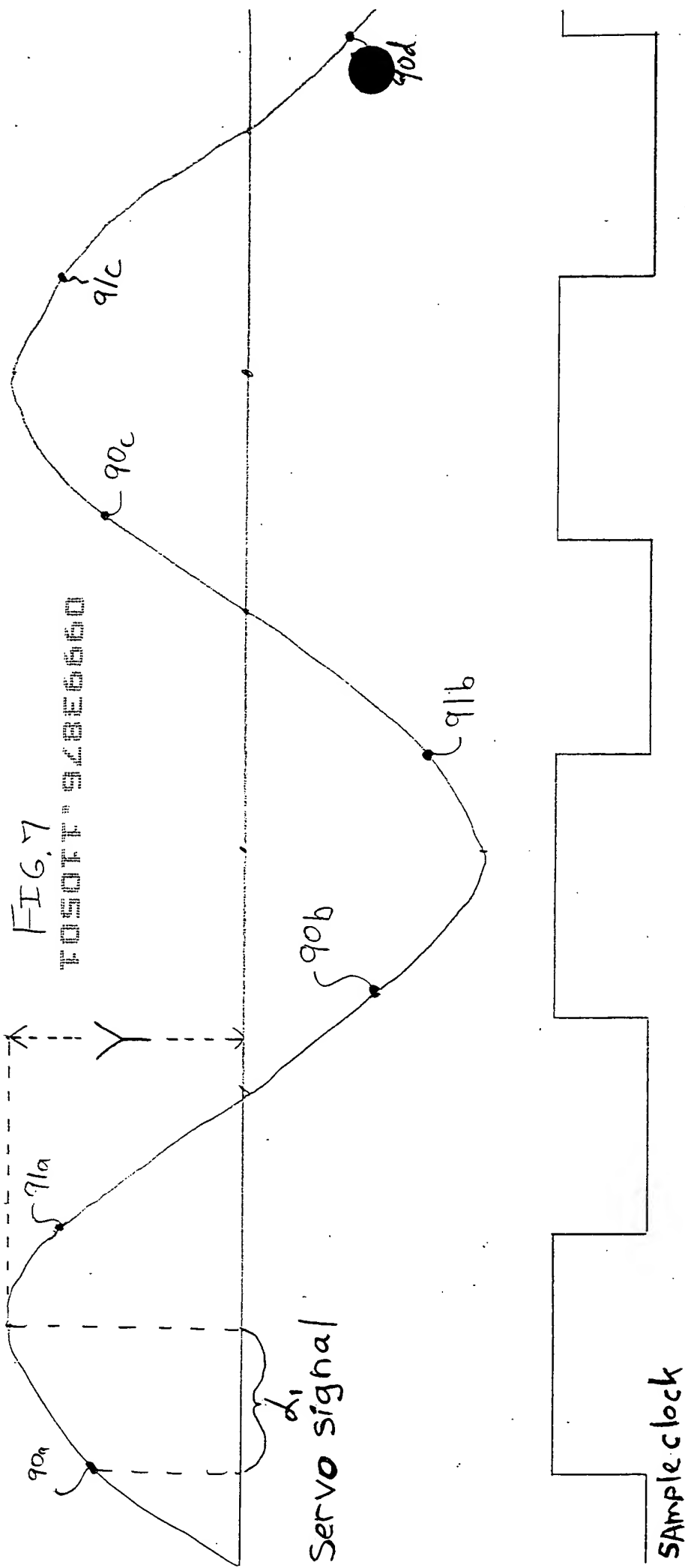


FIG. 14

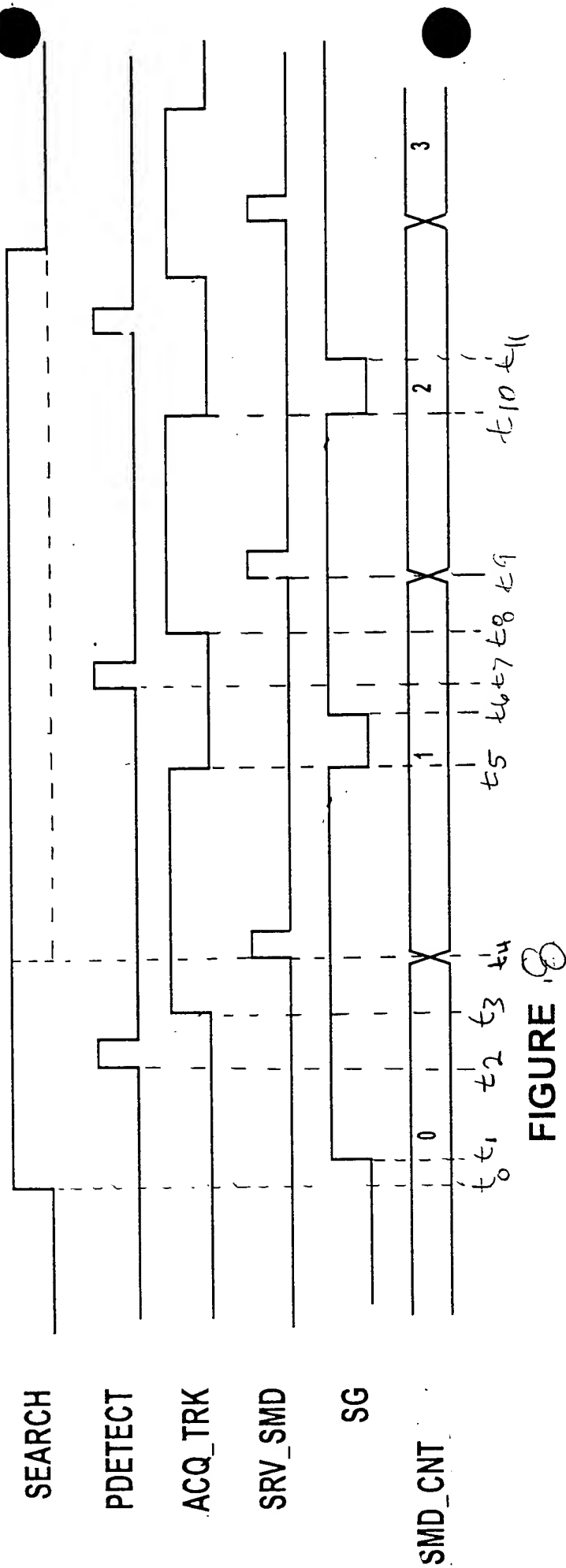


FIGURE 8

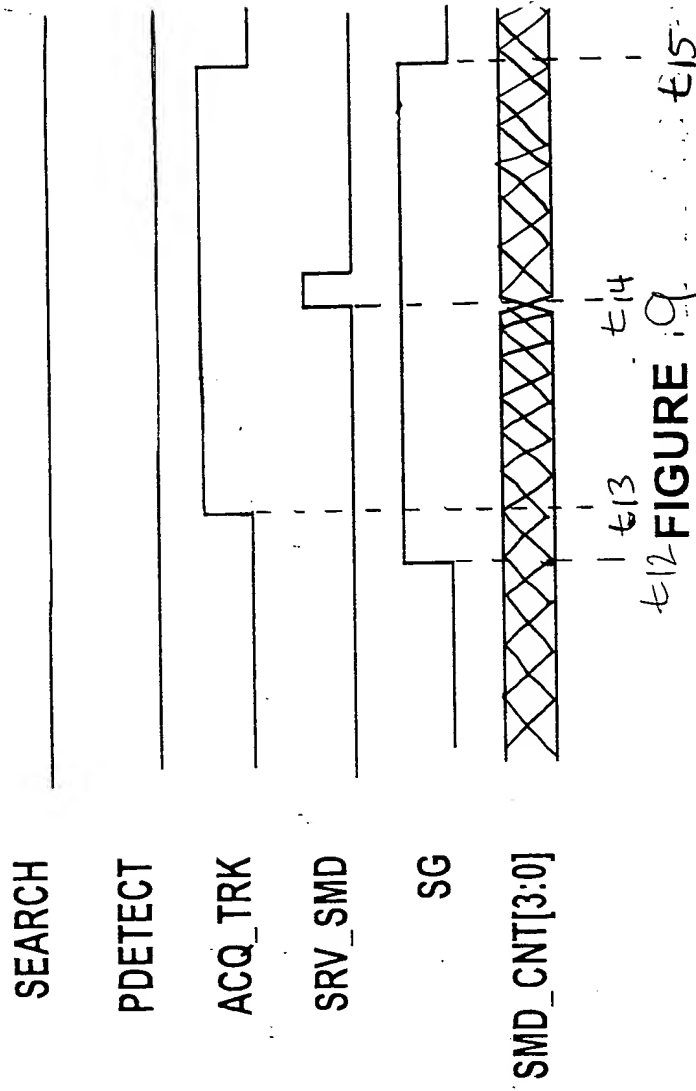


FIG. 5

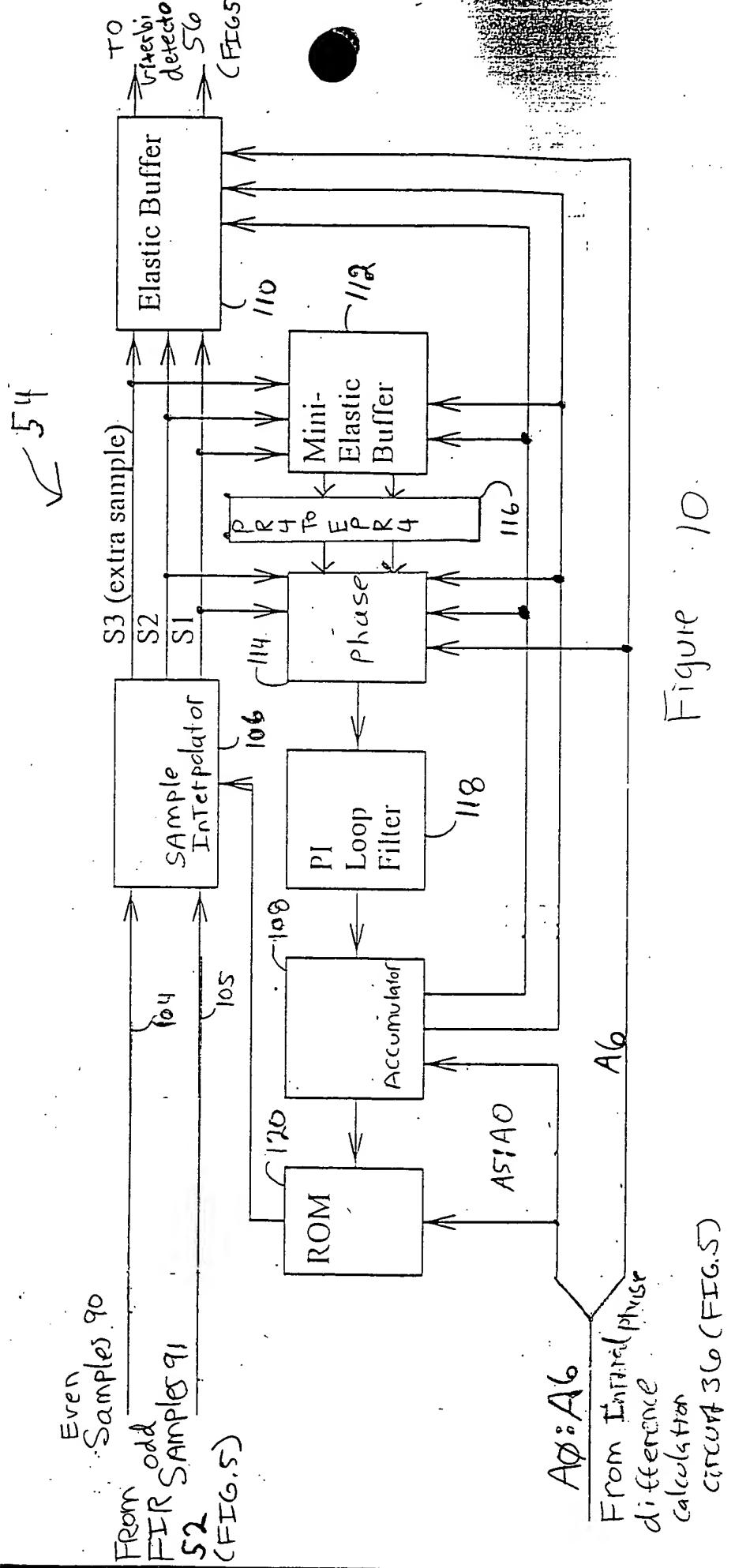


Figure 10

From Internal phase difference calculation circuit 36 (FIG. 5)

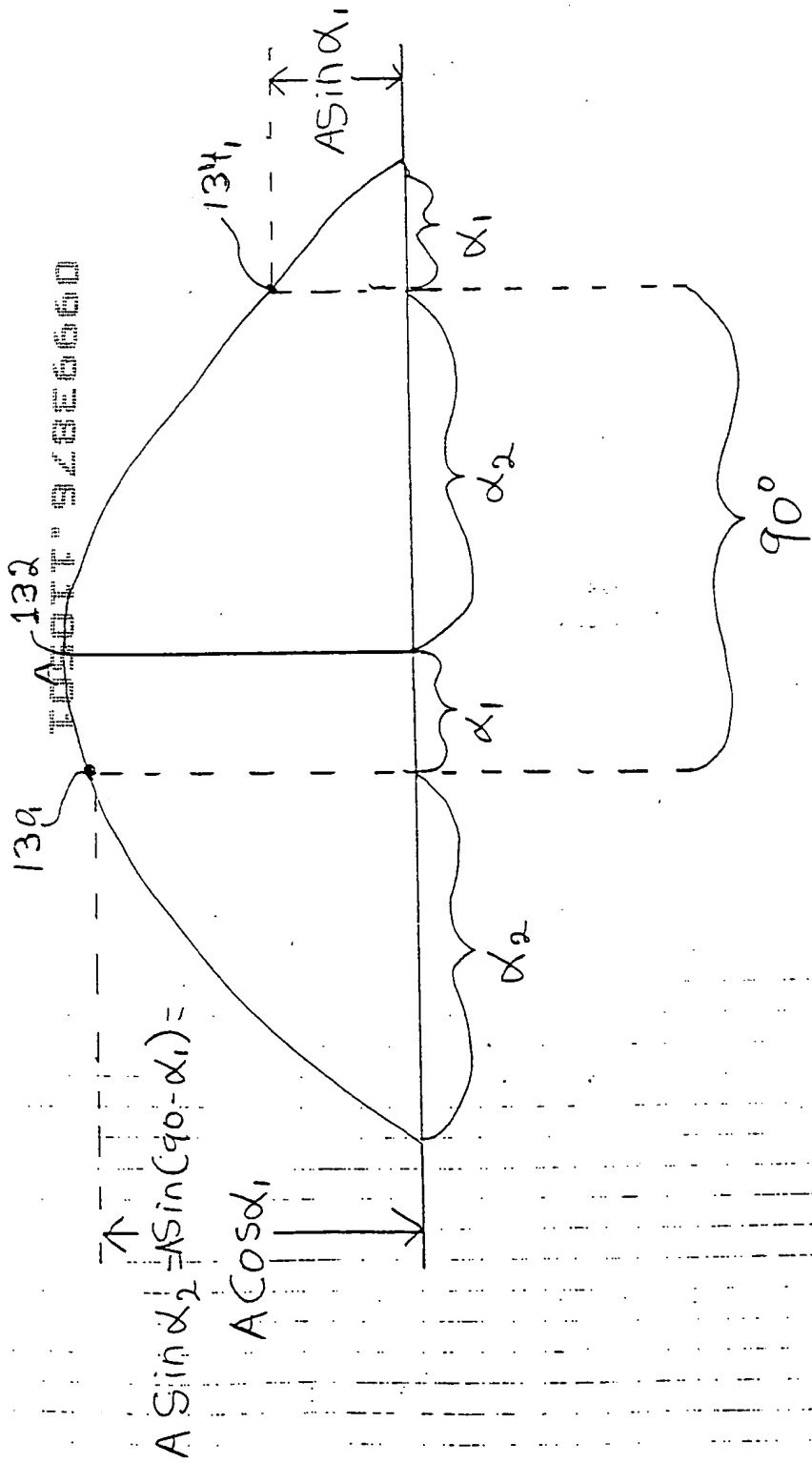


Figure 11



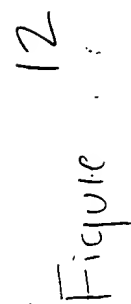
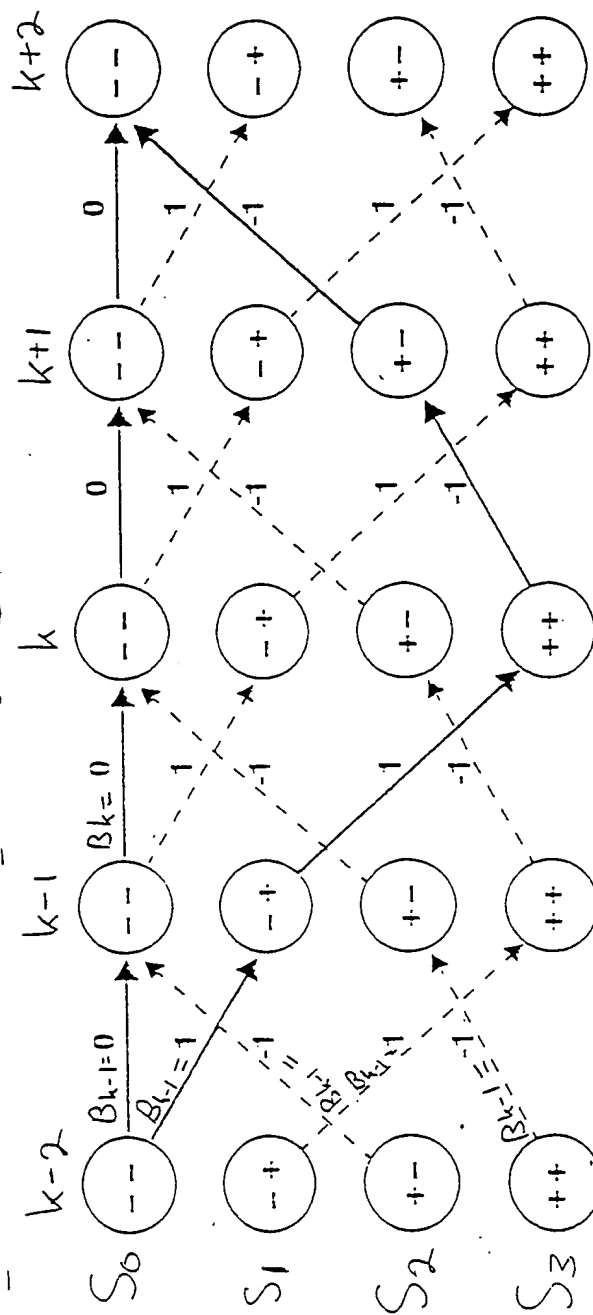


Figure 12

FIGURE 13



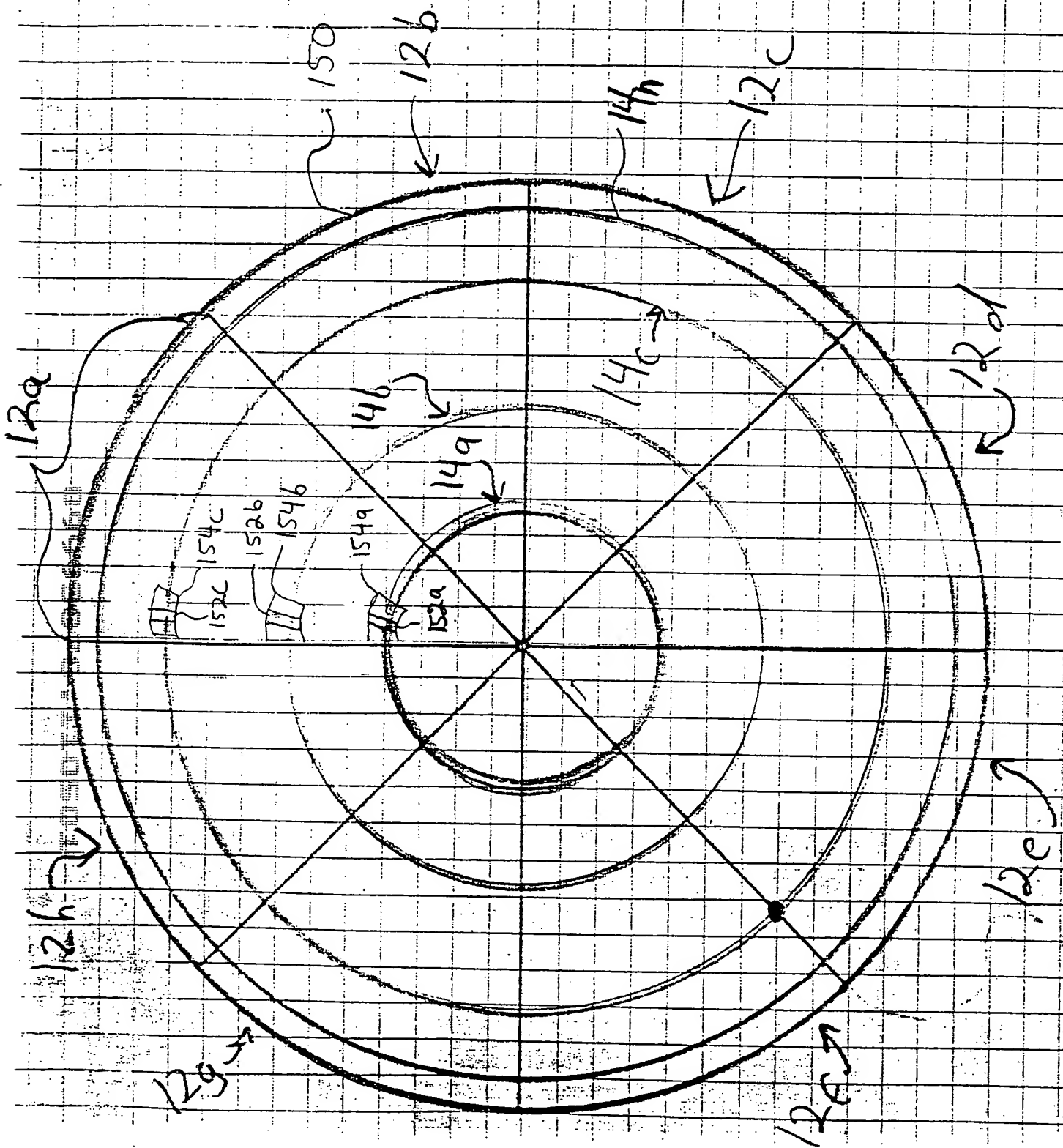


FIG. 15

TD50TF 928E6660

154a

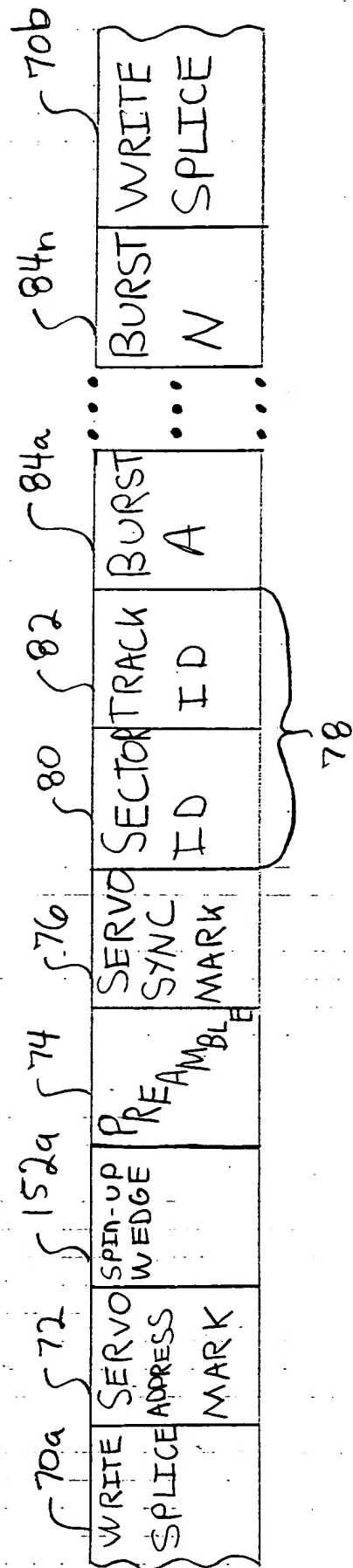


FIG. 16

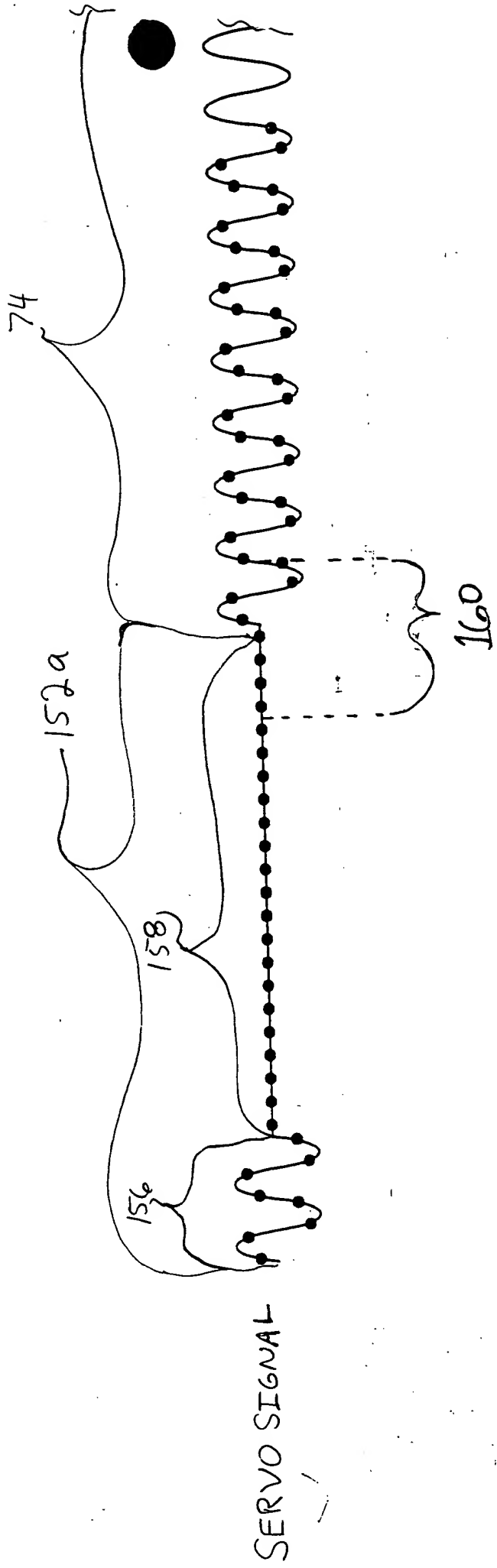


FIG. 17

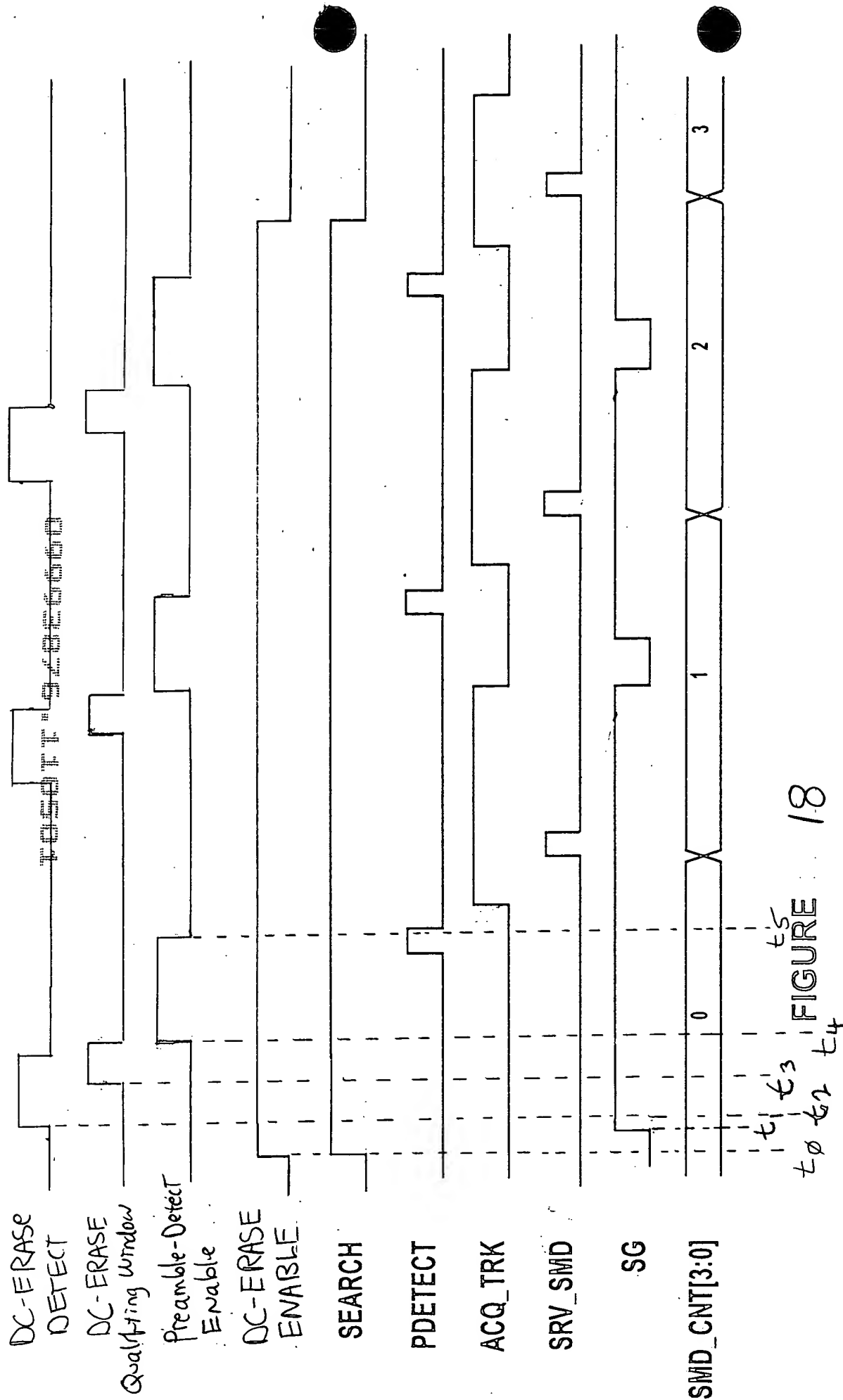


FIGURE 18

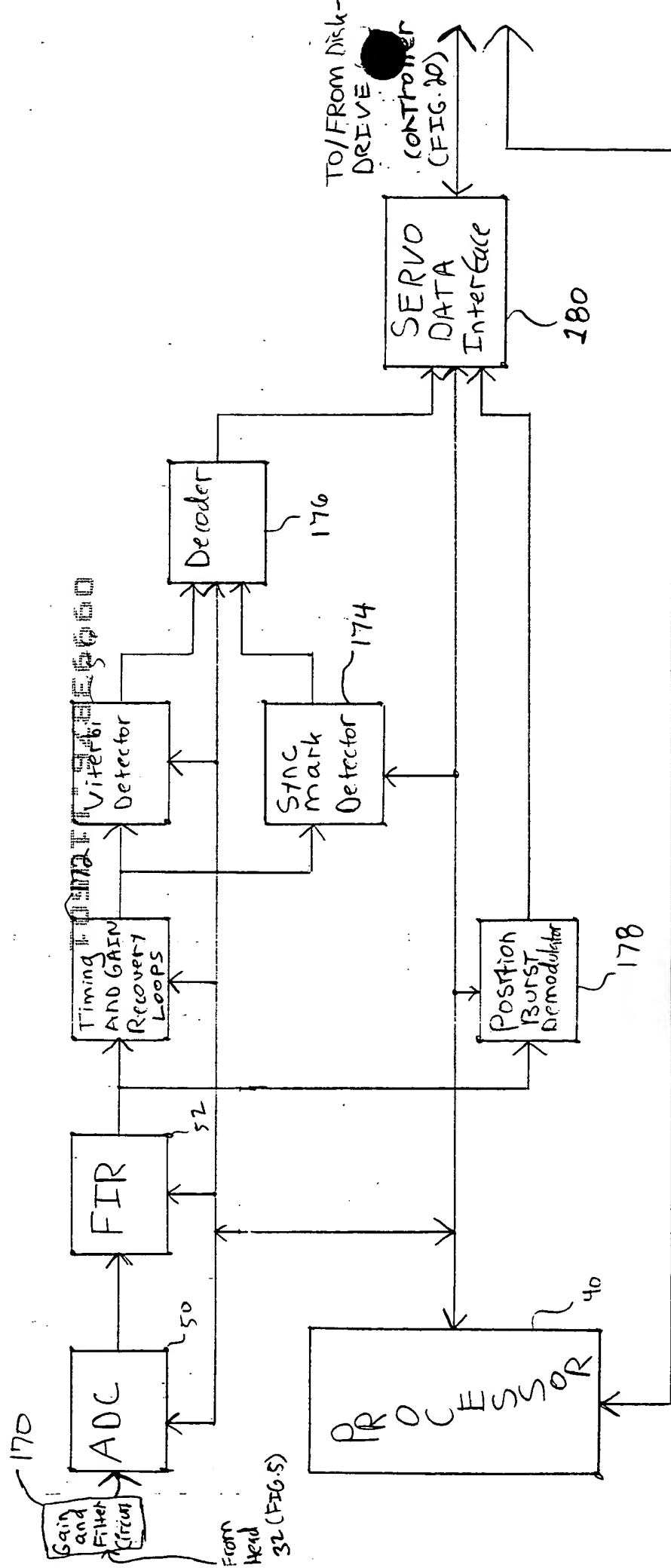


FIG. 19

30

FOOT 9285550

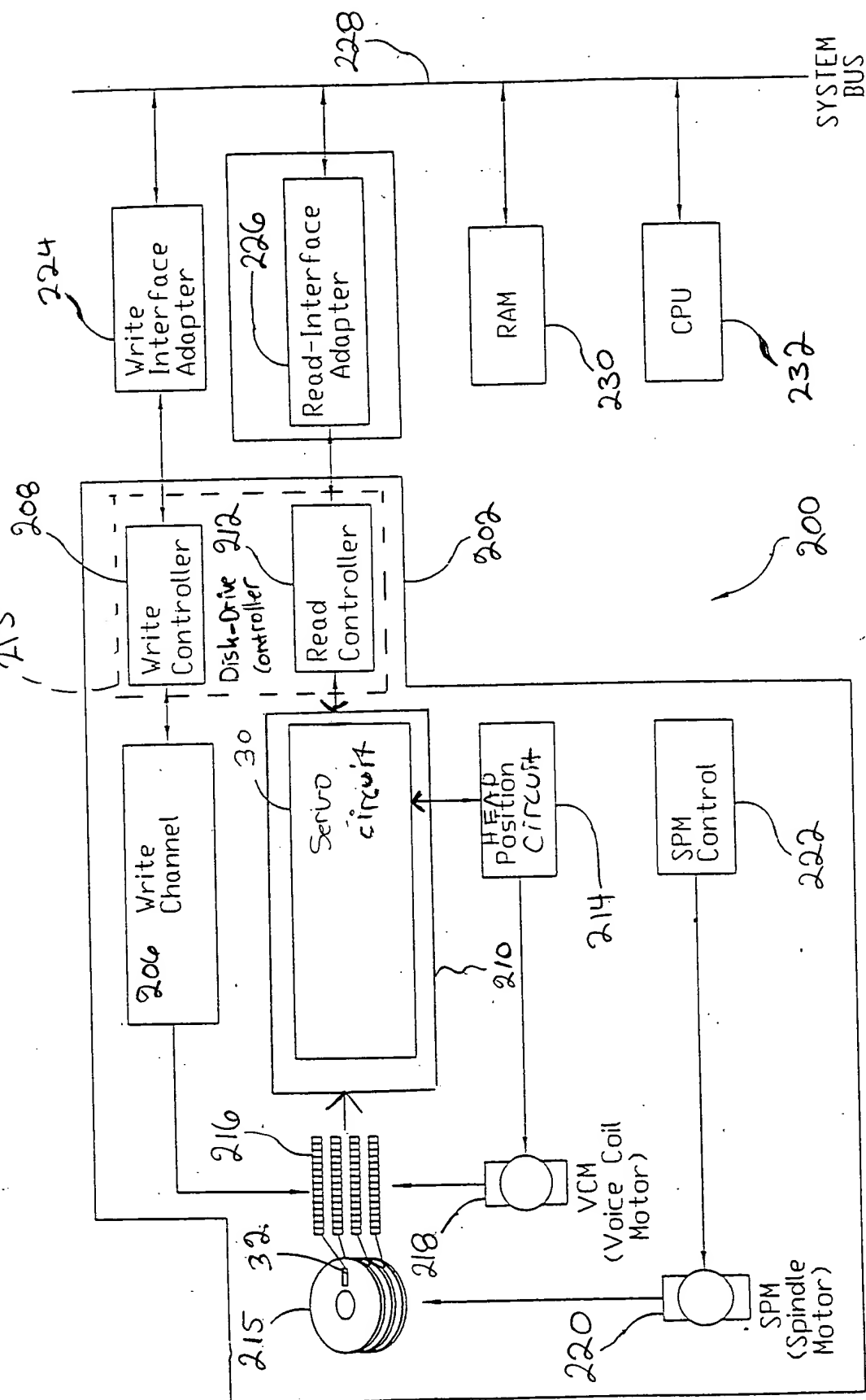


Fig. 20